

Europass Curriculum Vitae

Personal information

Surname(s) / First name(s) Email(s) Nationality(-ies) Date of birth Gender

Marinoni Mauro

September 2009 \rightarrow

Assistant Professor

Technologies" Ph.D.

m.marinoni@santannapisa.it Italian 15 November 1977 Male



Occupational field

Work experience

Dates

Occupation or position held Main activities and responsibilities

Name and address of employer Type of business or sector

Dates

Occupation or position held Main activities and responsibilities Name and address of employer Type of business or sector

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Name and address of employer Type of business or sector

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Occupation or position held Main activities and responsibilities Name and address of employer Type of business or sector

Education and training

Dates Title of qualification awarded Real-time embedded systems June 2003 - October 2003 Development of an embedded system

Hardware e Software development of an hexapod.

DIS - Università degli studi di Pavia - Via Ferrata, 1 - 27100 Pavia Research Organization - University

Evidence s.r.l. - Via Carducci 64/A - 56010 S.Giuliano Terme (PI)

November 2003 - October 2006 Ph.D in Computer Engineering

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April 2007 - August 2009 Postdoctoral researcher

Software development for microcontrollers

November 2006 - April 2007

board.

Research Organization - University

Postdoctoral researcher Analysis and development of power-aware scheduling algorithms and energymanagement kernel mechanisms for real-time embedded systems. TeCIP - Scuola Superiore Sant'Anna - via Moruzzi, 1 - 56127 Pisa Research Organization - University

Completion of the porting of the Erika Enterprise OSEK/VDX kernel for Microchip

16-bit microcontrollers and development of the Board Support Package for the Flex

Since July 2012: Leader of the "Resource Management" area at the ReTiS Laboratory. Since 2013: Committee member of the Graduate Programme in Computer

Science and Engineering and member of the Scientific board of the "Emerging Digital

Scuola Superiore Sant'Anna - Piazza Martiri della Libertà, 33 - 56127 Pisa - Italy

Information and Communication Technology (ICT)

Name and type of organization providing education and training Level in national or international classification

Dates Title of qualification awarded Finale grade Name and type of organization providing education and training Level in national or international classification

Dates Title of qualification awarded Finale grade Name and type of organization providing education and training Level in national or international classification

Personal skills and competences

Mother tongue(s) Altra/e lingua/e

Self-assessment European level^(*)

Inglese

Organisational skills and competences

Managerial skills and competences

Università degli Studi di Pavia

Doctor of Philosophy (ISCED Level 8 - Field 061)

September 1996 - July 2003 Master degree in Computer Engineering (B.Sc. and M.Sc.) 110/110 Università degli Studi di Pavia

Master degree (ISCED Level 7 - Field 061)

September 1991 - July 1996 Diploma di Perito Industriale Capotecnico in Informatica 56/60 Istituto Tecnico Industriale Statale "G. Cardano"- Pavia

Technical High School (ISCED Level 3)

Italian

Understanding		Speaking		Writing
Listening	Reading	Spoken interaction	Spoken production	
B2 Independent user	C1 Proficient user	C1 Proficient user	B2 Independent user	C1 Proficient user

^(*)Common European Framework of Reference (CEF) level

Participation in various regional, national and European research projects dealing with both the scientific part and the technical documentation for reporting. In particular:

- *RETINA*: European project founded by the Eurostars Programme to develop a software tool set that provides time-critical, predictable and reliable communication for automotive applications in heterogeneous systems and networks.
- JUNIPER: (Local coordinator) European project funded by the Seventh Framework Programme (FP7) focused on increasing performances and enforcing timing Constraints for applications dealing with large data streams (BigData).
- *PREDATOR*: European project funded by the Seventh Framework Programme (FP7) with the aim of improving the predictability of real-time systems with the main focus on the avionic and the automotive industries.
- ASCOLTA: Project funded by the Tuscany Region for the creation of a suitable infrastructure aimed at remote monitoring of vital parameters in patients suffering from heart failure.

Project Manager in subcontracts with regional and national companies for technology transfer activities.

Is and Cofounded in February 2013 BioCare Provider s.r.l., and remained parter till its acquisition by Camlin Group Ltd. in February 2018. The company uses the most modern web and mobile technologies to improve adherence to therapies, and it is accredited as a spin-off both at the Scuola Superiore Sant'Anna and the University of Pisa.

Technical skills and competences	Participation in the <i>technical program committee (TPC)</i> of international conferences such as JRWRTC 2011, ETFA 2012, MCSoC 2012, SIES 2014 (WiP chair), RTAS 2015, SIES 2015, SIES 2016, SIES 2017, SIES 2018, RTcMAS 2018. Reviewer for technical journals such as <i>Real-Time Systems Journal</i> , <i>Journal of the ACM</i> , <i>Journal of Systems Architecture</i> , <i>Journal of Computer and System Sci- ences</i> , <i>Transactions on Industrial Informatics</i> , <i>Transactions on Cyber-Physical Sys- tems</i> , <i>Transactions on Big Data</i> , , <i>Transactions on Embedded Computing Systems</i> , <i>Computer Networks</i> , <i>Journal on Emerging and Selected Topics in Circuits and Sys- tems</i> , and <i>MDPI Sensors</i> .	
Teaching experience	 A.Y. 2016-2017 and A.Y. 2017-2018: Projectual course of <i>Real-Time Software Development</i> in the Emerging Digital Technologies Ph.D at the Scuola Superiore Sant'Anna of Pisa. A.Y. 2016-2017 and A.Y. 2017-2018: Module of the course of <i>Embedded Systems</i> within the Master of Science in Embedded Systems at the University of Pisa. A.Y. 2015-2016, A.Y. 2016-2017, and A.Y. 2017-2018: Module of the course of <i>Component-based System Design</i> within the Master of Science in Embedded Systems at the University of Pisa. A.Y. 2013-2014, A.Y. 2014-2015, and A.Y. 2015-2016: Module of the course of <i>Industrial Applications</i> within the Master of Science in Embedded Systems at the University of Pisa. A.Y. 2013-2013, A.Y. 2014-2015, and A.Y. 2014-2015: Course of <i>Embedded Systems Laboratory</i> within the Master of Science in Embedded Systems at the University of Pisa. A.Y. 2012-2013, A.Y. 2013-2014, and A.Y. 2012-2013: Course of <i>Embedded Systems Laboratory</i> within the Master of Science in Embedded Systems at the University of Pisa. A.Y. 2010-2011, A.Y. 2011-2012, and A.Y. 2012-2013: Course of <i>Embedded Systems Laboratory</i> for Allievi Ordinari and Ph.D. students at the Scuola Superiore Sant'Anna of Pisa. A.Y. 2010-2011: Support for the course of <i>Embedded System Design</i> for Allievi Ordinari and Ph.D. students at the Scuola Superiore Sant'Anna of Pisa. Participation as a lecturer to the 2 editions of the Master "Smart Solutions – Smart Communities (SSSC)" organized by Scuola Sant'Anna in collaboration with Telecom Italia. Participation as a lecturer and head of the laboratory at different Summer Schools organized by the European Network of Excellence ARTIST DESIGN and sponsored by Microchip Technology Inc. June 2011: ARTIST Graduate School on RT Kernels for Microcontrollers. June 2008: Real-Time Kernels for Microcontrollers: Theory and Practice. March 2007: Real-Time Kernels for Microcontrollers: Theory and Practice.	
	 July 2006: First European Laboratory on Real-Time and Control for Embedded Systems 	
Driving licence	Car driving licence (Patente B)	
Publications		
Scientific pubblications	Publication of more than 50 papers in journals or peer-reviewed conference proceed- ings with an h-index of 12, computed by Google Schoolar. The Scopus profile has an h-index of 9, including 46 documents with 314 cita- tions, and is available online at https://www.scopus.com/authid/detail.uri?authorld= 7003668303.	

Patent Brondi Raffaello, Bannò Filippo, Bendinelli Sara, Sernissi Francesca, Castelli Christian, Mancina Antonio, Sartiano Daniele, Mauro Marinoni, *"Dispositivo e procedimento di compliance farmacologica"*, Italian Patent application No.Pl2012A000098 (September 20, 2012) and Italian patent N.1414455 (March 10, 2015).