

Workshop in memory of Marco Di Natale - Preliminary Program

January 30th, 2023 – Scuola Superiore Sant’Anna
Aula Magna, Piazza dei Martiri della Libertà, 33, Pisa, Italy

Monday, January 30th, 2023 – Morning Sessions		
08:20-09:00	Registration	
09:00-09:10	Greetings from the Rector Prof. Sabina Nuti	
09:10-09:30	Introduction by Prof. Giorgio Buttazzo	
09:30-10:30	Session 1: Remembering Marco, Part 1	
	09:30-09:50	Anna Acquaviva
	9:50-10:10	Paolo Gai (Evidence)
	10:10-10:30	Haibo Zeng (Virginia Tech, USA)
10:30-11:00	Coffee Break	
11:00-12:00	Session 2: Technical Presentations, Part 1	
	11:00-11:15	Evidence “Activities of Marco Di Natale in the Hauwei Research Center of Pisa”
	11:15-11:30	Silvio Bacci (Evidence) “A comprehensive framework for the analysis of automotive systems”
	11:30-11:45	Alessandro Cimatti (FBK, Italy) “Compositional formal verification of automotive systems”
	11:45-12:00	Francesco Paladino (SSSA, Italy) “Applying research results to system design: a timing analysis tool prototype”
12:00 - 13:00	Lunch	

Monday, January 30th, 2023 – Afternoon Sessions

13:00-13:50

Session 3: Remembering Marco, Part 2

13:00-13:10	Luigi Palopoli (University of Trento, Italy)
13:10-13:20	Qi Zhu (Northwestern University, USA)
13:20-13:30	Biruk Seyoum (Columbia University, USA)
13:30-13:40	Luis Almeida (University of Porto, Portugal)
13:40-13:50	Costantino Sabbatinelli (Resideo)

13:50-15:30

Session 4: Technical Presentations, Part 2

13:50-14:10	Enrico Bini (University of Turin, Italy) “Design and Optimization on Single Processor Real-Time Systems”
14:10-14:30	Björn Brandenburg (MPI-SWS, Germany) “A Pillar of Multiprocessor Real-Time Systems: An Appreciation of the MSRP”
14:30-14:50	Jian-Jia Chen (University of Dortmund, Germany) “Data Flow for Hard Real-time Distributed Systems: Freshness versus Responsiveness”
14:50-15:10	Paolo Pazzaglia (Bosch, Germany) “Optimizing the Functional Deployment on Multicore Platforms with Logical Execution Time”
15:10-15:30	Haibo Zeng (Virginia Tech, USA) “Real-Time for Model-based Design: an Essential Path to Safe Cyber-Physical Systems”

15:30-16:00

Coffee Break

16:10-17:50

Session 5: Technical Presentations, Part 3

16:10-16:30	David Broman (KTH, Sweden) “Modeling and Programming Cyber-Physical Systems: Time, Dynamics, and Uncertainty”
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	16:30-16:50	Luigi Palopoli (University of Trento, Italy) "Human-aware Motion Planning for Robots"
	16:50-17:10	Alessandro Biondi (SSSA, Italy) "Leveraging Logical Execution Time to Enhance Time-Predictability in Multicore Real-Time Systems"
	17:10-17:30	Daniel Casini (SSSA, Italy) "Optimized Partitioning and Priority Assignment of Real-Time Applications on Heterogeneous Platforms with Hardware Acceleration"
17:30-17:40	Closing Remarks	